Application/Control Number: 10/729,751                                        Page 4
Art Unit: 2115

> o   line 1, inserted after "wherein", -- selected gates of a field
>
>     programmable gate array are used to implement --;
>
>     *lines 1 and 2*
>
> o   ~~line 2~~, deleted "is implemented in a field programmable gate array"
>
>     after "element", inserted after "element", -- , --;
>
>     *2*
>
> o   line 3, inserted before "selected", -- said --.

The examiner notes that the claim following claim 46 is numbered as claim 48,
and that there is no claim 47 present in the application.  Since claim 41 (directed towards
the integrated circuit of claim 11) was cancelled, the examiner has added new claim 47
to replace the cancelled claim.  New claim 47 reads:

The integrated circuit of claim 11, further including:

said cycle counter generating a signal encoding what fraction of said
predetermined cycle count is reached; and

said repair processor receiving said signal and repairing said repairable circuit
element when said signal encodes a fraction of 1.

Any inquiry concerning this communication or earlier communications from the
examiner should be directed to Ji H. Bae whose telephone number is 571-272-7181.
The examiner can normally be reached on Monday-Friday, 10 am to 6:30 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's
supervisor, Thomas Lee can be reached on 571-272-3667.  The fax phone number for
the organization where this application or proceeding is assigned is 571-273-8300.

- Claim 15, line 3, deleted "generated" after "pulse", inserted after "pulse", -- generator --;

- Claim 16, line 4, deleted "is" after "repair processor";

- Claim 19,

  o line 1, inserted after "wherein", -- selected gates of a field programmable gate array are used to implement --;

  o line 2, deleted "is implemented in a field programmable gate array" after "element", inserted after "element", -- , --;

  o line 3, inserted before "selected", -- said --;

- Claim 22, line 3, deleted "generated" after "pulse", inserted after "pulse", -- generator --;

- Claim 33, line 2, inserted after "repairable", -- circuit --;

- Claim 34, line 1, deleted "said pulsed signal is" after "wherein", inserted after "wherein", -- said pulses comprise --;

- Claim 38,

  o line 1, inserted after "wherein", -- selected gates of a field programmable gate array are used to implement --;

  o line 2, deleted "is implemented in a field programmable gate array" after "element", inserted after "element", -- , --;

  o line 3, inserted before "selected", -- said --;

- Duplicate of claim 41, first instance, directed towards the integrated circuit of claim 11: CANCELLED.

- Claim 43, line 4, deleted "is" after "repair processor";

- Claim 46,